

Voltage Doubler Design And Analysis Ti

This book shows in a comprehensive presentation how Bond Graph methodology can support model-based control, model-based fault diagnosis, fault accommodation, and failure prognosis by reviewing the state-of-the-art, presenting a hybrid integrated approach to Bond Graph model-based fault diagnosis and failure prognosis, and by providing a review of software that can be used for these tasks. The structured text illustrates on numerous small examples how the computational structure superimposed on an acausal bond graph can be exploited to check for control properties such as structural observability and control lability, perform parameter estimation and fault detection and isolation, provide discrete values of an unknown degradation trend at sample points, and develop an inverse model for fault accommodation. The comprehensive presentation also covers failure prognosis based on continuous state estimation by means of filters or time series forecasting. This book has been written for students specializing in the overlap of engineering and computer science as well as for researchers, and for engineers in industry working with modelling, simulation, control, fault diagnosis, and failure prognosis in various application fields and who might be interested to see how bond graph modelling can support their work. Presents a hybrid model-based, data-driven approach to failure prognosis Highlights synergies and relations between fault diagnosis and failure prognostic Discusses the importance of fault diagnosis and failure prognostic in various fields

The electronics and information technology revolution continues, but it is a critical time in the development of technology. Once again, we stand on the brink of a new era where emerging

research will yield exciting applications and products destined to transform and enrich our daily lives! The potential is staggering and the ultimate impact is unimaginable, considering the continuing marriage of technology with fields such as medicine, communications and entertainment, to name only a few. But who will actually be responsible for transforming these potential new products into reality? The answer, of course, is today's (and tomorrow's) design engineers! The design of integrated circuits today remains an essential discipline in support of technological progress, and the authors of this book have taken a giant step forward in the development of a practice-oriented treatise for design engineers who are interested in the practical, industry-driven world of integrated circuit design.

"While integrated circuit (IC) power management has been an eternal topic for chip designers, inductor based DC-DC converters have been dominant in the field for years. However, because of the natures of inductors: large electro-magnetic interference, high coupling noise, and difficult silicon fabrication process, they are not favorable to on-chip solutions. Switched-capacitor (SC) DC-DC converters, which adopt capacitors for their energy storage components, have become increasingly popular among both the academia and the industry, because, apparently, they avoid the drawbacks of the inductor counterparts, and can be directly implemented on-chip without additional fabrication process. In this paper, we will investigate one of the most famous SC voltage doubler topologies, which is known as 'Favrat Cell'. By designing a chip, which converts 1.5 V voltage input to 2.5 V voltage output at 1 mA current load, we will walk through the details of a SC DC-DC converter design, including the switch cell, timing system, regulation loop and efficiency analysis. The design uses two 200 pF pumping capacitors and a 400 pF output capacitor in On-Semi half-micron technology. Four-

way interleaved phase structure is adopted to reduce the output voltage ripple. The gate-drive strategy of the switches has been improved to further reduce the reverse current injections during transitions. A new high-ratio voltage booster topology based on the cross-coupled topology has been introduced and will be discussed in comparison with the Dickson charge pump topology."--Abstract.

A critical part of ensuring that systems are advancing alongside technology without complications is problem solving. Practical applications of problem-solving theories can model conflict and cooperation and aid in creating solutions to real-world problems. *Soft-Computing-Based Nonlinear Control Systems Design* is a critical scholarly publication that examines the practical applications of control theory and its applications in problem solving to fields including economics, environmental management, and financial modelling. Featuring a wide range of topics, such as fuzzy logic, nature-inspired algorithms, and cloud computing, this book is geared toward academicians, researchers, and students seeking relevant research on control theory and its practical applications.

Analysis and Design of a Voltage Doubler Low DV-dT Easy to Build RectifierSwitched-capacitor Voltage Doubler Design Using 0.5 [micron] Technology

The book is a collection of high-quality peer-reviewed research papers presented in the Proceedings of International Conference on Power Electronics and Renewable Energy Systems (ICPERES 2014) held at Rajalakshmi Engineering College, Chennai, India. These research papers provide the latest developments in the broad area of Power Electronics and Renewable Energy. The book discusses wide variety of industrial, engineering and scientific applications of the emerging techniques. It presents invited papers from the

inventors/originators of new applications and advanced technologies.

This book contains extended and revised versions of the best papers presented at the 19th IFIP WG 10.5/IEEE International Conference on Very Large Scale Integration, VLSI-SoC 2011, held in Hong Kong, China, in October 2011. The 10 papers included in the book were carefully reviewed and selected from the 45 full papers and 16 special session papers presented at the conference. The papers cover a wide range of topics in VLSI technology and advanced research. They address the current trend toward increasing chip integration and technology process advancements bringing about stimulating new challenges both at the physical and system-design levels, as well as in the test of these systems.

This Special Issue with 35 published articles shows the significance of the topic “Signal Processing and Analysis of Electrical Circuit”. This topic has been gaining increasing attention in recent times. The presented articles can be categorized into four different areas: signal processing and analysis methods of electrical circuits; electrical measurement technology; applications of signal processing of electrical equipment; fault diagnosis of electrical circuits. It is a fact that the development of electrical systems, signal processing methods, and circuits has been accelerating. Electronics applications related to electrical circuits and signal processing methods have gained noticeable attention in recent times. The methods of signal processing and electrical circuits are widely used by engineers and scientists all over the world. The constituent papers represent a significant contribution to electronics and present applications that can be used in industry. Further improvements to the presented approaches are required for realizing their full potential.

This book provides various design techniques for switched-capacitor on-chip high-voltage

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generators, including charge pump circuits, regulators, level shifters, references, and oscillators. Readers will see these techniques applied to system design in order to address the challenge of how the on-chip high-voltage generator is designed for Flash memories, LCD drivers, and other semiconductor devices to optimize the entire circuit area and power efficiency with a low voltage supply, while minimizing the cost.

This book presents state-of-the-art analog and power management IC design techniques for various wireless power transfer (WPT) systems. To create elaborate power management solutions, circuit designers require an in-depth understanding of the characteristics of each converter and regulator in the power chain. This book addresses WPT design issues at both system- and circuit-level, and serves as a handbook offering design insights for research students and engineers in the integrated power electronics area.

This step-by-step guide provides the reader with a detailed and thorough introduction to practical antenna design and model implementation. In this book, Hubregt J. Visser provides an introduction to the fundamentals of antenna design and the implementation of design models. A variety of antennas for wireless applications and communications systems are explained, and the real-life use of the antennas is demonstrated through extensive use of application examples. The author includes discussions on the design process of several antennas, such as intravascular MR Antennas, PCB antennas, RFID antennas, rectennas etc. Furthermore, emphasis is placed on Computer Aided Design (CAD) using approximated models. Key Features Includes coverage on intravascular MR Antennas, PCB antennas, RFID antennas, rectennas, etc. Comprehensively details the application areas, modeling, analysis, and validation processes for individual antennas. Discusses the use of equivalent dipole antennas,

equivalent transmission line networks and electrostatics Introduces many antennas and models that have not been covered in previous publications (such as MRI Antennas, for example) This book will be of interest to microwave and antenna engineers. Graduate and post-graduate antennas students studying BSc and MSc courses, as well as research assistants will also find this book insightful.

This book provides a detailed analysis of all aspects of capacitive DC-DC converter design: topology selection, control loop design and noise mitigation. Readers will benefit from the authors' systematic overview that starts from the ground up, in-depth circuit analysis and a thorough review of recently proposed techniques and design methodologies. Not only design techniques are discussed, but also implementation in CMOS is shown, by pinpointing the technological opportunities of CMOS and demonstrating the implementation based on four state-of-the-art prototypes.

Lists citations with abstracts for aerospace related reports obtained from world wide sources and announces documents that have recently been entered into the NASA Scientific and Technical Information Database.

This book focuses on the latest emerging technologies in electric vehicles (EV), and their economic and environmental impact. The topics covered include different types of EV such as hybrid electrical vehicle (HEV), battery electrical vehicle (BEV), fuel cell electrical vehicle (FCEV), plug-in hybrid electrical vehicle (PHEV). Theoretical background and practical examples of conventional electrical machines, advanced electrical machines, battery energy sources, on-board charging and off-board charging techniques, and optimization methods are presented here. This book can be useful for students, researchers and practitioners interested

in different problems and challenges associated with electric vehicles.

This book constitutes the refereed proceedings of the 7th IFIP WG 5.5/SOCOLNET Advanced Doctoral Conference on Computing, Electrical and Industrial Systems, DoCEIS 2016, held in Costa de Caparica, Portugal, in April 2016. The 53 revised full papers were carefully reviewed and selected from 112 submissions. The papers present selected results produced in engineering doctoral programs and focus on research, development, and application of cyber-physical systems. Research results and ongoing work are presented, illustrated and discussed in the following areas: enterprise collaborative networks; ontologies; Petri nets; manufacturing systems; biomedical applications; intelligent environments; control and fault tolerance; optimization and decision support; wireless technologies; energy: smart grids, renewables, management, and optimization; bio-energy; and electronics.

This book consists of peer-reviewed papers presented at the First International Conference on Intelligent Computing in Control and Communication (ICCC 2020). It comprises interesting topics in the field of applications of control engineering, communication and computing technology. As the current world is witnessing the use of various intelligent techniques for their independent problem solving, so this book may have a wide importance for all range of researchers and scholars. The book serves as a reference for researchers, professionals and students from across electrical, electronic and computer engineering disciplines.

This research book volume offers an important learning opportunity with insights into a variety of emerging electronic circuit aspects, such as new materials, energy harvesting architectures, and compressive sensing technique. Advanced circuit technologies are extremely powerful and developed rapidly. They change industry. They change lives. And we know they can change

the world. The exhibition on these new and exciting topics will benefit readers in related fields. This book presents a system-level analysis of inductive wireless power transfer (WPT) links. The basic requirements, design parameters, and utility of key building blocks used in inductive WPT links are presented, followed by detailed theoretical analysis, design, and optimization procedure, while considering practical aspects for various application domains. Readers are provided with fundamental, yet easy to follow guidelines to help them design high-efficiency inductive links, based on a set of application-specific target specifications. The authors discuss a wide variety of recently proposed approaches to achieve the maximum efficiency point, such as the use of additional resonant coils, matching networks, modulation of the load quality factor (Q-modulation), and adjustable DC-DC converters. Additionally, the attainability of the maximum efficiency point together with output voltage regulation is addressed in a closed-loop power control mechanism. Numerous examples, including MATLAB/Octave calculation scripts and LTspice simulation files, are presented throughout the book. This enables readers to check their own results and test variations, facilitating a thorough understanding of the concepts discussed. The book concludes with real examples demonstrating the practical application of topics discussed. Covers both introductory and advanced levels of theory and practice, providing readers with required knowledge and tools to carry on from simple to advanced wireless power transfer concepts and system designs; Provides theoretical foundation throughout the book to address different design aspects; Presents numerous examples throughout the book to complement the analysis and designs; Includes supplementary material (numerical and circuit simulation files) that provide a "hands-on" experience for the reader; Uses real examples to demonstrate the practical application of

topics discussed.

Integrated Audio Amplifiers in BCD Technology is the first book to describe the design at Audio Amplifiers using a Bipolar CMOS DMOS (BCD) process. It shows how the combination of the 3 processes, made available by advances in process technology, gives rise to the design of more robust and powerful audio amplifiers which can be more easily implemented in digital and mixed-signal circuits. Integrated Audio Amplifiers in BCD Technology starts with an introduction to audio amplifiers which includes a comparison of amplifier classes, general design considerations and a list of specifications for integrated audio power amplifiers. This is followed by an extensive discussion of the properties of DMOS transistors which are the key components in BCD technologies. Then the theory and the design of chargepump circuits is considered. In most BCD technologies only n-type DMOS transistors are available. Therefore a boosted supply voltage is required to achieve rail-to-rail output capability which can be generated with a chargepump. The new solutions that are found can also be used for many applications where DC-DC conversion with low output ripple is needed. Finally the design of audio power amplifier in BCD technology is discussed. The design concentrates on a new quiescent control circuit with very high ratio between quiescent current and maximum output current and on the output stage topologies. The problem of controlling the DMOS output transistors over a wide range of currents either saturated or non saturated requires a completely new design of the driving circuits that utilize of the special properties of the DMOS transistor. Integrated Audio Amplifiers in BCD Technology is essential reading for practising analog design engineers and researchers in the field. It is also suitable as a text for an advanced course on the subject. With a foreword by Ed van Tuijl.

Design of Low-Voltage Low-Power CMOS Delta-Sigma A/D Converters investigates the feasibility of designing Delta-Sigma Analog to Digital Converters for very low supply voltage (lower than 1.5V) and low power operation in standard CMOS processes. The chosen technique of implementation is the Switched Opamp Technique which provides Switched Capacitor operation at low supply voltage without the need to apply voltage multipliers or low V_t MOST devices. A method of implementing the classic single loop and cascaded Delta-Sigma modulator topologies with half delay integrators is presented. Those topologies are studied in order to find the parameters that maximise the performance in terms of peak SNR. Based on a linear model, the performance degradations of higher order single loop and cascaded modulators, compared to a hypothetical ideal modulator, are quantified. An overview of low voltage Switched Capacitor design techniques, such as the use of voltage multipliers, low V_t MOST devices and the Switched Opamp Technique, is given. An in-depth discussion of the present status of the Switched Opamp Technique covers the single-ended Original Switched Opamp Technique, the Modified Switched Opamp Technique, which allows lower supply voltage operation, and differential implementation including common mode control techniques. The restrictions imposed on the analog circuits by low supply voltage operation are investigated. Several low voltage circuit building blocks, some of which are new, are discussed. A new low voltage class AB OTA, especially suited for differential Switched Opamp applications, together with a common mode feedback amplifier and a comparator are presented and analyzed. As part of a systematic top-down design approach, the non-ideal charge transfer of the Switched Opamp integrator cell is modeled, based upon several models of the main opamp non-ideal characteristics. Behavioral simulations carried out with these

models yield the required opamp specifications that ensure that the intended performance is met in an implementation. A power consumption analysis is performed. The influence of all design parameters, especially the low power supply voltage, is highlighted. Design guidelines towards low power operation are distilled. Two implementations are presented together with measurement results. The first one is a single-ended implementation of a Delta-Sigma ADC operating with 1.5V supply voltage and consuming 100 μ W for a 74 dB dynamic range in a 3.4 kHz bandwidth. The second implementation is differential and operates with 900 mV. It achieves 77 dB dynamic range in 16 kHz bandwidth and consumes 40 μ W. Design of Low-Voltage Low-Power CMOS Delta-Sigma A/D Converters is essential reading for analog design engineers and researchers.

This book discusses in detail the CMOS implementation of energy harvesting. The authors describe an integrated, indoor light energy harvesting system, based on a controller circuit that dynamically and automatically adjusts its operation to meet the actual light circumstances of the environment where the system is placed. The system is intended to power a sensor node, enabling an autonomous wireless sensor network (WSN). Although designed to cope with indoor light levels, the system is also able to work with higher levels, making it an all-round light energy harvesting system. The discussion includes experimental data obtained from an integrated manufactured prototype, which in conjunction with a photovoltaic (PV) cell, serves as a proof of concept of the desired energy harvesting system.

This book gathers high-quality research papers presented at the First International Conference, ICSC 2019, organised by THDC Institute of Hydropower Engineering and Technology, Tehri, India, from 20 to 21 April 2019. The book is divided into two major sections

– Intelligent Computing and Smart Communication. Some of the areas covered are Parallel and Distributed Systems, Web Services, Databases and Data Mining Applications, Feature Selection and Feature Extraction, High-Performance Data Mining Algorithms, Knowledge Discovery, Communication Protocols and Architectures, High-speed Communication, High-Voltage Insulation Technologies, Fault Detection and Protection, Power System Analysis, Embedded Systems, Architectures, Electronics in Renewable Energy, CAD for VLSI, Green Electronics, Signal and Image Processing, Pattern Recognition and Analysis, Multi-Resolution Analysis and Wavelets, 3D and Stereo Imaging, and Neural Networks.

Chip-integrated power management solutions are a must for ultra-low power systems. This enables not only the optimization of innovative sensor applications. It is also essential for integration and miniaturization of energy harvesting supply strategies of portable and autonomous monitoring systems. The book particularly addresses interfaces for energy harvesting, which are the key element to connect micro transducers to energy storage elements. Main features of the book are: - A comprehensive technology and application review, basics on transducer mechanics, fundamental circuit and control design, prototyping and testing, up to sensor system supply and applications. - Novel interfacing concepts - including active rectifiers, MPPT methods for efficient tracking of DC as well as AC sources, and a fully-integrated charge pump for efficient maximum AC power tracking at sub-100W ultra-low power levels. The chips achieve one of widest presented operational voltage range in standard CMOS technology: 0.44V to over 4.1V. - Two special chapters on analog circuit design – it studies benefits and obstacles on implemented chip prototypes with three goals: ultra- low power, wide supply voltage range, and integration with standard technologies.

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Alternative design approaches are pursued using bulk-input transistor stages in forward-bias operation for amplifiers, modulators, and references. - Comprehensive Appendix – with additional fundamental analysis, design and scaling guidelines, circuit implementation tables and dimensions, schematics, source code listings, bill of material, etc. The discussed prototypes and given design guidelines are tested with real vibration transducer devices. The intended readership is graduate students in advanced courses, academics and lecturers, R&D engineers.

Analog Circuit Design contains the contribution of 18 tutorials of the 20th workshop on Advances in Analog Circuit Design. Each part discusses a specific to-date topic on new and valuable design ideas in the area of analog circuit design. Each part is presented by six experts in that field and state of the art information is shared and overviewed. This book is number 20 in this successful series of Analog Circuit Design, providing valuable information and excellent overviews of: Topic 1 : Low Voltage Low Power, chairman: Andrea Baschirotto Topic 2 : Short Range Wireless Front-Ends, chairman: Arthur van Roermund Topic 3 : Power Management and DC-DC, chairman : Michiel Steyaert. Analog Circuit Design is an essential reference source for analog circuit designers and researchers wishing to keep abreast with the latest development in the field. The tutorial coverage also makes it suitable for use in an advanced design course.

This book gathers selected research papers presented at the International Conference on Power, Control and Communication Infrastructure 2019 (ICPCCI 2019), organized by the Institute of Infrastructure, Technology, Research and Management (IITRAM), Ahmedabad, Gujarat, India, on July 4–5, 2019. It presents the latest advances, trends and challenges in

control system technologies and infrastructures. The book addresses a range of solutions to the problems faced by engineers and researchers to design and develop controllers for emerging areas like smart grid, integration of renewable energy, automated highway systems, haptics, unmanned aerial vehicles, sensor networks, robotics, formation control and many more. The solutions discussed in this book encourage and inspire researchers, industry professionals and policymakers to put these methods into practice.

This book is a collection of selected research papers presented at the International Conference on Innovations in Electrical and Electronics Engineering (ICIEEE 2019), which was organized by the Guru Nanak Institutions, Ibrahimpatnam, Hyderabad, Telangana, India, on July 26–27, 2019. The book highlights the latest developments in electrical and electronics engineering, especially in the areas of power systems, power electronics, control systems, electrical machinery, and renewable energy. The solutions discussed here will encourage and inspire researchers, industry professionals, and policymakers to put these methods into practice. This exciting new book focuses on the analysis and design of reconfigurable antennas for modern wireless communications, sensing, and radar. It presents the definitions of basic antenna parameters, an overview of RF switches and explains how to characterize their insertion loss, isolation, and power handling issues. Basic reconfigurable antenna building blocks, such as dipoles, monopoles, patches and slots are described, followed by presentations on frequency reconfigurable antennas, pattern reconfigurable antennas, and basic scanning antenna arrays. Switch biasing in an electromagnetic environment is discussed, as well as simulation strategies of reconfigurable antennas, and MIMO (Multiple Input Multiple Output) reconfigurable antennas. Performance characterization of reconfigurable

antennas is also presented. The book provides information for the technical professional to design frequency reconfigurable, pattern reconfigurable, and MIMO antennas all relevant for modern wireless communication systems. Readers learn how to select switching devices, bias them properly, and understand their role in the overall reconfigurable antenna design. The book presents practical experimental implementation issues, including losses due to switches, materials, and EMI (Electromagnetic Interference) and shows how to address those.

Synthesis of Computational Structures for Analog Signal Processing focuses on analysis and design of analog signal processing circuits. The author presents a multitude of design techniques for improving the performances of analog signal processing circuits, and proposes specific implementation strategies that can be used in CMOS technology. The author's discussion proceeds from the perspective of signal processing as it relates to analog. Included are coverage of low-power design, portable equipment, wireless nano-sensors and medical implantable devices. The material is especially appropriate for researchers and specialists in the area of analog and mixed-signal CMOS VLSI design, as well as postgraduate or Ph.D. students working on analog microelectronics.

This book provides readers specializing in ultra-low power supply design for self-powered applications an invaluable reference on reconfigurable switched capacitor power converters. Readers will benefit from a comprehensive introduction to the design of robust power supplies for energy harvesting and self-power applications, focusing on the use of reconfigurable switched capacitor based DC-DC converters, which is ideal for such applications. Coverage includes all aspects of switched capacitor power supply designs, from fundamentals, to reconfigurable power stages, and sophisticated controller designs.

This volume comprises select proceedings of ETAEERE-2016. The volume offers state-of-the-art chapters on energy management systems (EMS), renewable energy resources, micro-generation, green communications architectures and frameworks, green computing and education as well as energy-aware process optimization. The contents covers a wide variety of topics and aspects including management of renewable energy systems and environmental challenges. The contents of this volume will be useful to researchers and practicing engineers working in the areas of smart grids and renewable energy generation, distribution, and management.

The desire to use railguns in defense applications has elevated the level of concentration in all areas of the railgun system. Necessary in any railgun is a large amount of electric power to deliver the required force to the projectile. One popular source of power is high voltage capacitor banks. The NPS Railgun Lab employs a fully functioning railgun with capacitor banks as power supplies. A reliable and safe charging supply for these capacitor banks is desirable and investigated in this paper. Construction and testing of a power supply charger is compared to simulation results. The power supply charger consists of a Voltage Source Inverter (VSI) connected to a high voltage boost transformer; the output of the transformer is connected to a voltage doubler rectifier; the output of the rectifier charges the high voltage capacitor to 9 kV in two minutes. The power supply controller is an FPGA (Field Programmable Gate Array) with embedded control to ensure the safe operation of the power supply.

MICROELECTRONIC CIRCUITS: ANALYSIS AND DESIGN, 3E combines a breadth-first approach to learning electronics with a strong emphasis on design and simulation. This book first introduces the general characteristics of circuits (ICs) in preparation for using circuit design

and analysis techniques. This edition then offers a more detailed study of devices and circuits and how they operate within ICs. More than half of the problems and examples concentrate on design and emphasize how to use computer software tools extensively. The book's proven sequence introduces electronic devices and circuits, then electronic circuits and applications, and finally, digital and analog integrated circuits. Readers learn to apply theory to real-world design problems as they master the skills to test and verify their designs. Important Notice: Media content referenced within the product description or the product text may not be available in the ebook version.

The book focuses on the integration of intelligent communication systems, control systems, and devices related to all aspects of engineering and sciences. It contains high-quality research papers presented at the 2nd international conference, ICICCD 2017, organized by the Department of Electronics, Instrumentation and Control Engineering of University of Petroleum and Energy Studies, Dehradun on 15 and 16 April, 2017. The volume broadly covers recent advances of intelligent communication, intelligent control and intelligent devices. The work presented in this book is original research work, findings and practical development experiences of researchers, academicians, scientists and industrial practitioners.

Radio Frequency IDentification (RFID) stores and retrieves data using devices called RFID tags: objects attached to or incorporated into a product, animal or person which communicate with an RFID reader or interrogator. This book proposes a linear two-port model for an N-stage modified-Greinacher full wave rectifier, predicting the overall conversion efficiency at low power levels where the diodes are operating near their threshold voltage. Included is an experimental procedure to measure how impedance modulation in the tag affects the signal at the reader,

and a useful tool for choosing the most appropriate impedances.

CMOS Processors and Memories addresses the-state-of-the-art in integrated circuit design in the context of emerging computing systems. New design opportunities in memories and processor are discussed. Emerging materials that can take system performance beyond standard CMOS, like carbon nanotubes, graphene, ferroelectrics and tunnel junctions are explored. CMOS Processors and Memories is divided into two parts: processors and memories. In the first part we start with high performance, low power processor design, followed by a chapter on multi-core processing. They both represent state-of-the-art concepts in current computing industry. The third chapter deals with asynchronous design that still carries lots of promise for future computing needs. At the end we present a “hardware design space exploration” methodology for implementing and analyzing the hardware for the Bayesian inference framework. This particular methodology involves: analyzing the computational cost and exploring candidate hardware components, proposing various custom architectures using both traditional CMOS and hybrid nanotechnology CMOL. The first part concludes with hybrid CMOS-Nano architectures. The second, memory part covers state-of-the-art SRAM, DRAM, and flash memories as well as emerging device concepts.

Semiconductor memory is a good example of the full custom design that applies various analog and logic circuits to utilize the memory cell’s device physics. Critical physical effects that include tunneling, hot electron injection, charge trapping (Flash memory) are discussed in detail. Emerging memories like FRAM, PRAM and ReRAM that depend on magnetization, electron spin alignment, ferroelectric effect, built-in potential well, quantum effects, and thermal melting are also described. CMOS Processors and Memories is a must for anyone serious

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about circuit design for future computing technologies. The book is written by top notch international experts in industry and academia. It can be used in graduate course curriculum. This book presents a collection of peer-reviewed articles from the 7th International Conference on Microelectronics, Circuits, and Systems – Micro 2020. The volume covers the latest development and emerging research topics of material sciences, devices, microelectronics, circuits, nanotechnology, system design and testing, simulation, sensors, photovoltaics, optoelectronics, and its different applications. This book also deals with several tools and techniques to match the theme of the conference. It will be a valuable resource for researchers, professionals, Ph.D. scholars, undergraduate and postgraduate students working in Electronics, Microelectronics, Electrical, and Computer Engineering.

This volume emphasizes the design and development of advanced switched-opamp architectures and techniques for low-voltage low-power switched-capacitor systems. It presents a novel multi-phase switched-opamp technique together with new system architectures that are critical in improving significantly the performance of switched-capacitor systems at low supply voltages.

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